

**Notice of References Cited**

Application/Control No.

10/041,796

Applicant(s)/Patent Under  
Reexamination  
CHEUNG ET AL.

Examiner

Natalia Figueroa

Art Unit

2651

Page 1 of 1

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Low-Noise High-Bandwidth Voltage-Biasing MR-Element Preamplifier with Short Circuit Protection", November 1, 1993, IBM Technical Disclosure Bulletin, Volume Number 36, Issue Number 11, Pages 113-114.
	V	"Low Voltage Preamp Front End Stage for Magneto-Resistive Heads", March 1, 1993, IBM Technical Disclosure Bulletin, Volume Number 36, Issue Number 3, Pages 393-396.
	W	"DC MR Preamplifier with Short Circuit Protection", November 1, 1993, IBM Technical Disclosure Bulletin, Volume Number 36, Issue Number 11, Pages 585-586.
	X	Klein et al, "A 0.8nV/√Hz CMOS Preamplifier for IC-Magneto-Resistive Read Elements", December 1994, IEEE Journal of Solid-State Circuits, Volume 29, Number 12, Pages 1589-1595.

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.